

SCHEMATIC OF THE  
LASER RECEIVER

Model ETA-4200

NOTES:

1. ALL RESISTORS ARE 1/4-WATT, 5% TOLERANCE UNLESS OTHERWISE NOTED. (K=1000, M=1,000,000.)
2. CAPACITORS LESS THAN 1 ARE IN  $\mu\text{F}$  (MICROFARADS); 1 OR GREATER ARE IN  $\mu\text{F}$ , UNLESS OTHERWISE NOTED.
3. THE FOLLOWING SYMBOLS ARE USED ON THIS SCHEMATIC:

▽ THIS SYMBOL DENOTES A CIRCUIT BOARD GROUND.

○ THIS SYMBOL DENOTES A CIRCUIT BOARD WIRE CONNECTION.

○ THIS SYMBOL DENOTES A DC VOLTAGE TAKEN WITH A HIGH-INPUT IMPEDANCE VOLTMETER FROM THE POINT INDICATED TO CIRCUIT BOARD GROUND.

4. TO FIND THE PHYSICAL LOCATION OF PARTS, REFER TO THE "CIRCUIT BOARD X-RAY VIEW" ON MANUAL PAGE 24.

